



**Product Information** 

**DATE**: 05. Apr. 2012

SAMSUNG TFT-LCD

MODEL: LTA320AN12-W

<u>The Information Described in this Specification is Preliminary and can be changed without prior notice</u>

Samsung Display Co., LTD.

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# **Revision History**

**Samsung Secret** 

Date	Rev. No	Page	Summary
05. Apr. 2012	000	All	First issued

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### **General Description**

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#### **Description**

**LTA320AN12** is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 32.0" is 1366 x 768 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV

#### **Features**

- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with wide color gamut
- PVA (Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- HD resolution (16:9)
- Low Power consumption
- Edge Type LED (Light Emitted Diode) BLU
- DE (Data Enable) mode
- 1Ch LVDS (Low Voltage Differential Signaling) interface (1pixel/clock)

#### **General Information**

Items	Specification	Unit	Note
	734.4 (H) X 430.3 (V)		±1.0mm
Module Size	33.7 (D max)	mm	With wall mount stud
Weight	5300 (Max)	g	
Pixel Pitch	0.51075 (H) × 0.17025 (V)	mm	
Active Display Area	697.6845 (H) X 392.256 (V)	mm	
Surface Treatment	Hard-coating		Anti-glare
Display Colors	8 bit - 16.7 M	colors	
Number of Pixels	1366 x 768	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	320 (Typ.)	cd/m <sup>2</sup>	
		_	

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# 1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V <sub>DD</sub>	GND-0.3	16	<b>V</b>	(1)
Storage temperature	T <sub>STG</sub>	-20	60	°C	(2)
Operating temperature	T <sub>OPR</sub>	0	50	°C	(2)
Shock ( non - operating )	X,Y,Z	-	50	G	(3)
Vibration ( non - operating )	V <sub>NOP</sub>	-	1.5	G	(4)

Note (1) Ta= 25  $\pm$  2 °C

- (2) Temperature and relative humidity range are shown in the figure below.
  - a. 90 % RH Max. (Ta ≤ 39 °C)
  - b. Relative Humidity is 90% or less. (Ta > 39 °C)
  - c. No condensation
- (3) 11ms, sine wave, one time for  $\pm X$ ,  $\pm Y$ ,  $\pm Z$  axis
- (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

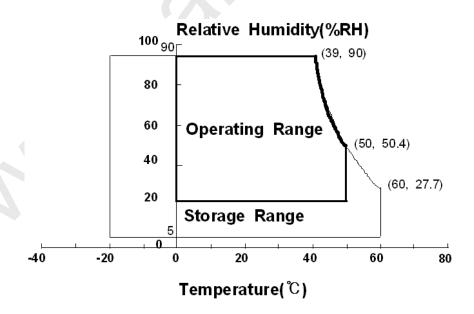


Fig. Temperature and Relative humidity range

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# 2. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON RD-80S, TOPCON SR-3, ELDIM EZ-Contrast

(Ta =  $25 \pm 2$ °C, VDD=12V, fv= 60Hz, f<sub>DCLK</sub> = 78MHz, Duty = 100%)

						-DCLK -		
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R		-	4000	-		(1) SR-3
Response Time	G-to-G	Tg		-	20	-	msec	(3) RD-80S
Luminance of (Center of so		Y <sub>L</sub>		-	320	-	cd/m <sup>2</sup>	(4) SR-3
	Red	Rx	Normal		0.648			
	Red	Ry	q <b>L,R</b> =0 q <b>U,D</b> =0		0.335			
	Green	Gx	q <b>0,D</b> =0		0.316			
Color Chromaticity	Green	Gy	Viewing	TYP.	0.606	TYP.		(5),(6)
(CIE 1931)	Blue	Bx	Angle	-0.03	0.151	+0.03		SR-3
	Dide	Ву			0.059			
	White	Wx			0.280			
	VVIIIC	Wy			0.290			
Color Gar	mut	-		-	70	-	%	(5)
Color Tempe	erature	-		-	10000	-	K	SR-3
	Hor.	$q_L$		79	89	-		
Viewing	пог.	$q_R$	C/R≥10	79	89	-	Degree	(6)
Angle	Ver.	q <sub>U</sub>	O/Nº 10	79	89	-	Degree	EZ-Contrast
	vel.	$q_D$		79	89	ı		
White Brigh Uniformi (9 Points	ty	B <sub>uni</sub>		1	1	30	%	(2) SR-3

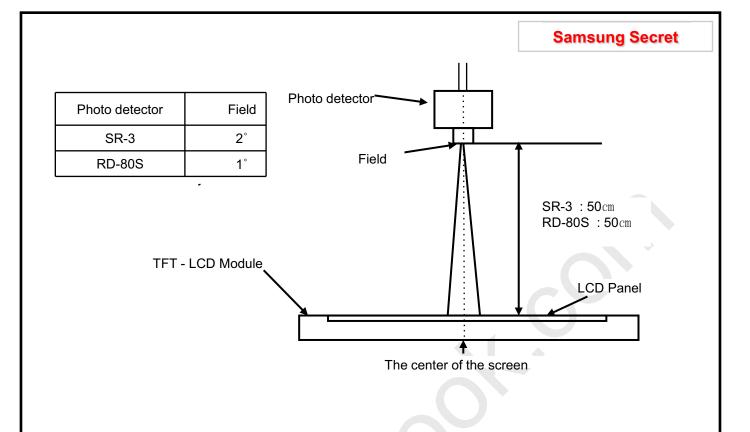
#### - Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

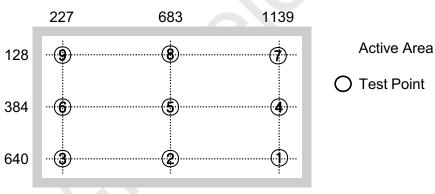
Environment condition : Ta = 25  $\pm$  2  $^{\circ}$ C

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- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax: Luminance with all pixels white Gmin: Luminance with all pixels black

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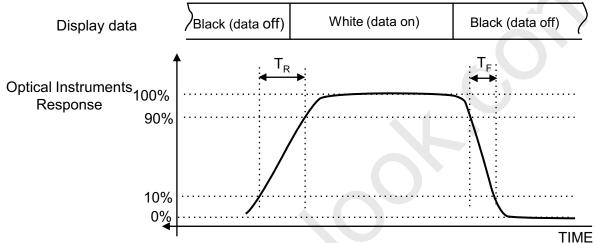


Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White )

$$Buni = 100* \frac{(B \max - B \min)}{B \max}$$

Bmax: Maximum brightness Bmin: Minimum brightness

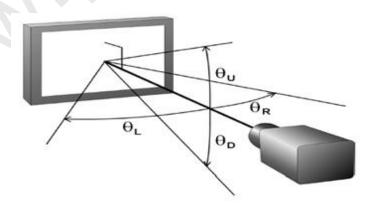
Note (3) Definition of Response time: Sum of Tr, Tf



Note (4) Definition of Luminance of White: Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931) Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle : Viewing angle range (C/R ≥10)



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# 3. Electrical Characteristics

#### 3.1 TFT LCD Module

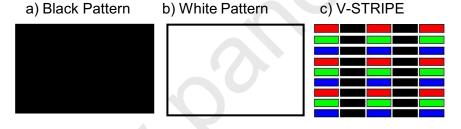
The connector for display data & timing signal should be connected.

Ta =  $25^{\circ}$ C  $\pm$  2  $^{\circ}$ C

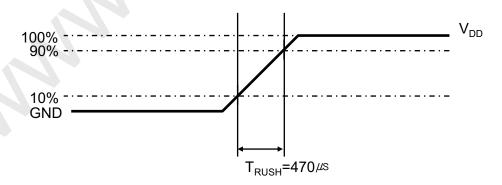
Item		Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of P	Voltage of Power Supply		10.8	12.0	13.2	V	(1)
Current of (a) Black	(a) Black		-	400	-	mA	
Power	(b) White	I <sub>DD</sub>	-	500	-	mA	(2),(3)
Supply	(c) V-STRIPE		-	600	700	mA	
Vsync Frequ	Vsync Frequency		50	60	66	Hz	
Hsync Frequ	Hsync Frequency		44	48	53	kHz	
Main Frequency		f <sub>DCLK</sub>	72	78	85	MHz	
Rush Currer	nt	I <sub>RUSH</sub>	-		4	Α	(4)

Note (1) The ripple voltage should be controlled under 10% of  $V_{DD}$ .

- (2) fV=60Hz, fDCLK=78MHz,  $V_{DD}=12.0V$ , DC Current.
- (3) Power dissipation check pattern (LCD Module only)



(4) Measurement Conditions



Rush Current  $I_{RUSH}$  can be measured when  $T_{RUSH}$ . is 470 $\mu$ s.

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## 3.2 Back Light Unit

The back light unit contains Edge type White LEDs (Light Emitting Diode)

Ta=25  $\pm$  2°C

# **LCD Module**

Converter

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Operating Life Time	Hr	-	30,000	1	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta =  $25\pm2\,^{\circ}\mathrm{C}$ , For LED package only. ]

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# 3.3 Converter Input Condition & Specification

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Items	Cumbal	Conditions	SI	pecification	ns	Unit	Note	
nems	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note	
Input Voltage	Vin	-	22	24	26	V	<b>Ta=25</b> ±2 °C	
Input Current	I <sub>RUSH</sub>	Vin=24.0V Vdim =3.3V	-	-	2.5	А	Ó	
Output Current	I <sub>O,MAX</sub>	Vin = 24.0V V dim =3.3 V	124	130	136	mArms		
Backlight	ON	Vin=24.0 V	3.0	-	5.25	V		
On/Off	OFF	Vin=24.0 V	0	-	0.4	V		
Dimming Range	V_ <sub>DIM</sub>	Vin :22~26V	0	-	3.3	V	(2)	
Dimming Duty	D max	Vin=24V Dim:3.3V	100	_	-	%	(2)	
Output	D min	Vin=24V Dim:0V		1	1	70	(2)	
Dimming Frequency	F <sub>PWM</sub>	Vin=24.0 V	140	150	160	Hz	(2)	
External Dimming Duty Range	EX_Dim	Min	1	-	100	%		
External Dimming Frequency Range			138	-	164	Hz	Dim Pin(#13) : Floating	
External Dimming	V	High (ON)	3.0	-	5.25	V		
Signal Level	$V_{PWM}$	Low (Off)	0	-	0.4	V V		

Note) Power Consumption is measured when 320 [cd/m ] of luminance which is the typical luminance.

(1) All data is measured after 120min warm-up.

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# 4. Input Terminal Pin Assignment

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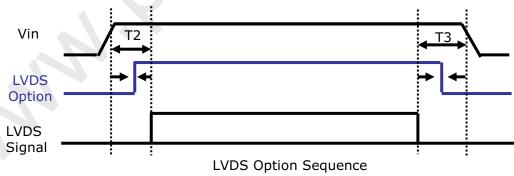
Connector: IS100-L300-C23

### 4.1. Input Signal & Power

PIN No.	Description	PIN No.	Description
1	VIN (12V)	16	LV1_P
2	VIN (12V)	17	GND
3	VIN (12V)	18	LV2_N
4	VIN (12V)	19	LV2_P
5	N.C	20	GND
6	GND	21	LVCLK_N
7	GND	22	LVCLK_P
8	N.C	23	GND
9	LVDS_SEL (note 2)	24	LV3_N
10	N.C	25	LV3_P
11	GND	26	GND
12	LV0_N	27	N.C
13	LV0_P	28	N.C
14	GND	29	N.C
15	LV1_N	30	GND

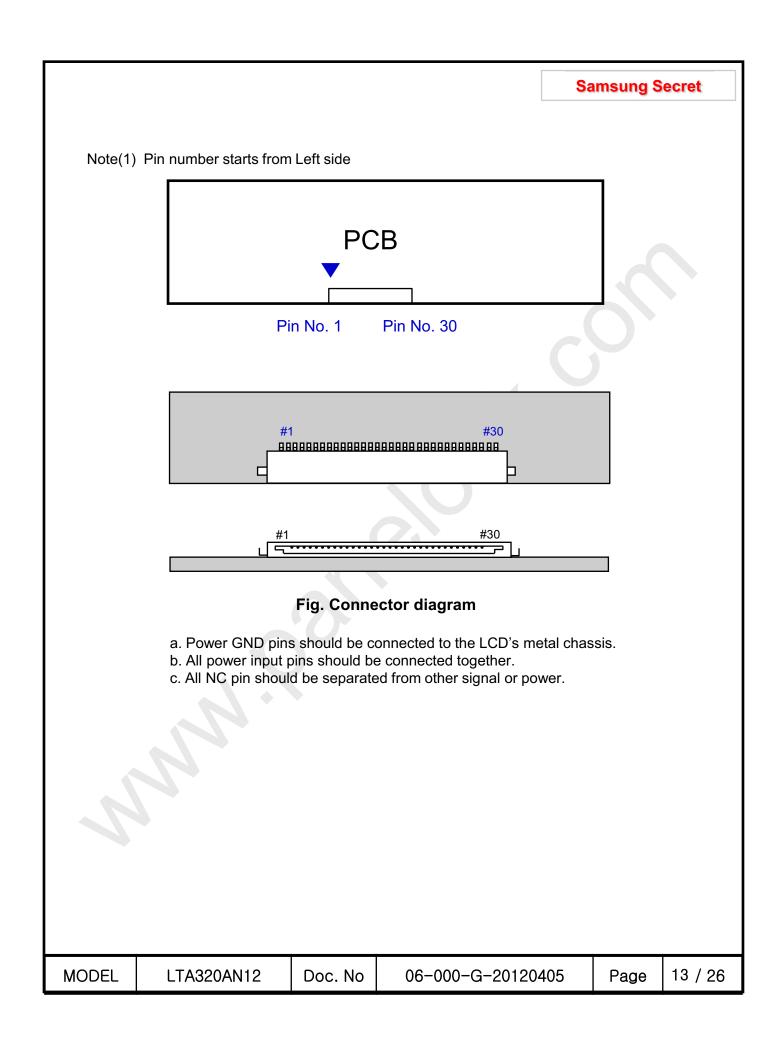
Note1) No Connection: This PINS are only used ONLY for SAMSUNG. Note2) LVDS OPTION : If this PIN is HIGH (3.3 V)  $\rightarrow$  Normal LVDS format LOW (GND)  $\rightarrow$  JEIDA LVDS format

SEQUENCE : On =  $VDD(T1) \ge LVDS$  Option  $\ge Interface Signal(T2)$ OFF = Interface Signal(T3)  $\ge LVDS$  Option  $\ge VDD$ 



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## 4.2. converter Input Pin Configuration

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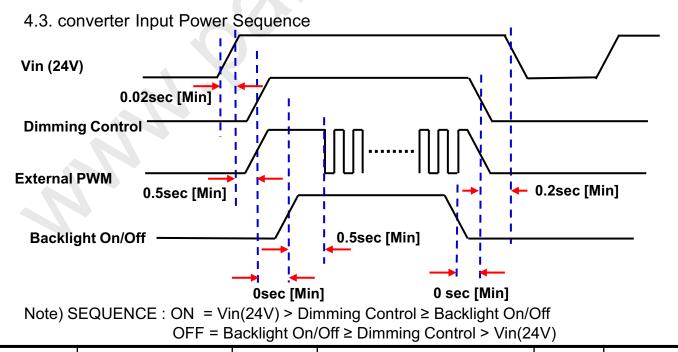
Connector: Yeon-ho, 20022WR-14B1

Din No	Pin Configuration(FUNCTION)
Pin No.	Master
1	24 V
2	24 V
3	24 V
4	24 V
5	24 V
6	GND
7	GND
8	GND
9	GND
10	GND
11	No connection
12	ENA [ converter on/off control signal ]
13	Dimming Control [0V:Min, 3.3V:Max] *Note(1)
14	External PWM [1~100%] *Note(1)

Note(1) If use Dimming Control, Pin 14 Must be N.C If use External PWM, Pin 13 Must be N.C

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# 4.4 LVDS Interface

LVDS Receiver : Tcon (merged)Data Format (JEIDA & VESA)

		LVDS pin		JEIDA -DATA	VESA -	DATA	
		TxIN/RxOU	ТО	R2	R0		
		TxIN/RxOU	T1	R3	R1		
		TxIN/RxOU	T2	R4	R2	R2	
TxOU	T/RxIN0	TxIN/RxOU	Т3	R5	R3		
		TxIN/RxOU	T4	R6	R4	,	
		TxIN/RxOU	T6	R7	R5		
		TxIN/RxOU	Т7	G2	G0		
		TxIN/RxOU	Т8	G3	G1		
		TxIN/RxOU	Т9	G4	G2		
		TxIN/RxOUT	12	G5	G3	1	
TxOU	T/RxIN1	TxIN/RxOUT	13	G6	G4		
		TxIN/RxOUT	<sup>-</sup> 14	G7	G5		
		TxIN/RxOUT	15	B2	В0		
		TxIN/RxOUT	18	B3	B1		
		TxIN/RxOUT	19	B4	B2		
		TxIN/RxOUT	20	B5	В3		
		TxIN/RxOUT	21	В6	B4		
TxOU	T/RxIN2	TxIN/RxOUT	22	B7	B5	B5	
		TxIN/RxOUT	<sup>-</sup> 24	HSYNC	HSYN	HSYNC	
		TxIN/RxOUT	25	VSYNC	VSYN	1C	
		TxIN/RxOUT	<sup>-</sup> 26	DEN	DEN	١	
		TxIN/RxOUT	27	R0	R6		
		TxIN/RxOU	T5	R1	R7		
		TxIN/RxOUT	10	G0	G6		
TxOU	T/RxIN3	TxIN/RxOUT	11	G1	G7		
		TxIN/RxOUT	16	В0	В6		
		TxIN/RxOUT	17	B1	В7		
		TxIN/RxOUT	23	RESERVED	RESER	VED	
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# 4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

												D	ATA S	SIGNA	AL.											GRAY
COLOR	DISPLAY (8bit)				RE	ED							GRE	EN					_		BL	UE				SCALE
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	В1	B2	ВЗ	B4	B5	В6	В7	LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	ı
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
BASIC	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE	1	:	:	:	:	:	:			:	:	:	:	:	:				:	:	:	:	:			R3~
OF RED	↓	:	:	:	:	:	:			:	:	:	:	:	:			): `	:	:	:	:	:			R252
	LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
	DARK	0	0	0	0	0	0	0	0<	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE	1	<u>:</u>	:	:	:	:	:				::	:	:	:	:			:	:	:	:	:	:			G3~
OF GREEN	<b>↓</b>	:	:	:	:	:	:				:	:	:	:	:			:	:	:	:	:	:			G252
	LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
	BLACK	0	0	0	0	0 <	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
CDAY	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
GRAY SCALE	1			:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~
OF BLUE	1	<u>:</u>	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B252
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray:

Rn: Red Gray, Gn: Green Gray, Bn: Blue Gray (n = Gray level) Input Signal: 0 = Low level voltage, 1 = High level voltage

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# 5. Interface Timing

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## 5.1 Timing Parameters ( DE mode )

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock		1/T <sub>C</sub>	72	78	85	MHz	-
Hsync	Frequency	F <sub>H</sub>	44	48	53	KHz	-
Vsync		F <sub>V</sub>	50	60	66	Hz	-
Vertical	Active Display Period	$T_{VD}$	-	768	-	Lines	-
Display Term	Vertical Total	T <sub>V</sub>	780	802	1200	Lines	-
Horizontal	Active Display Period	T <sub>HD</sub>	-	1366	-	Clocks	-
Display Term	Horizontal Total	T <sub>H</sub>	1460	1624	2000	clocks	-

Note) This product is DE mode. But the Hsync & Vsync signal must be inputted

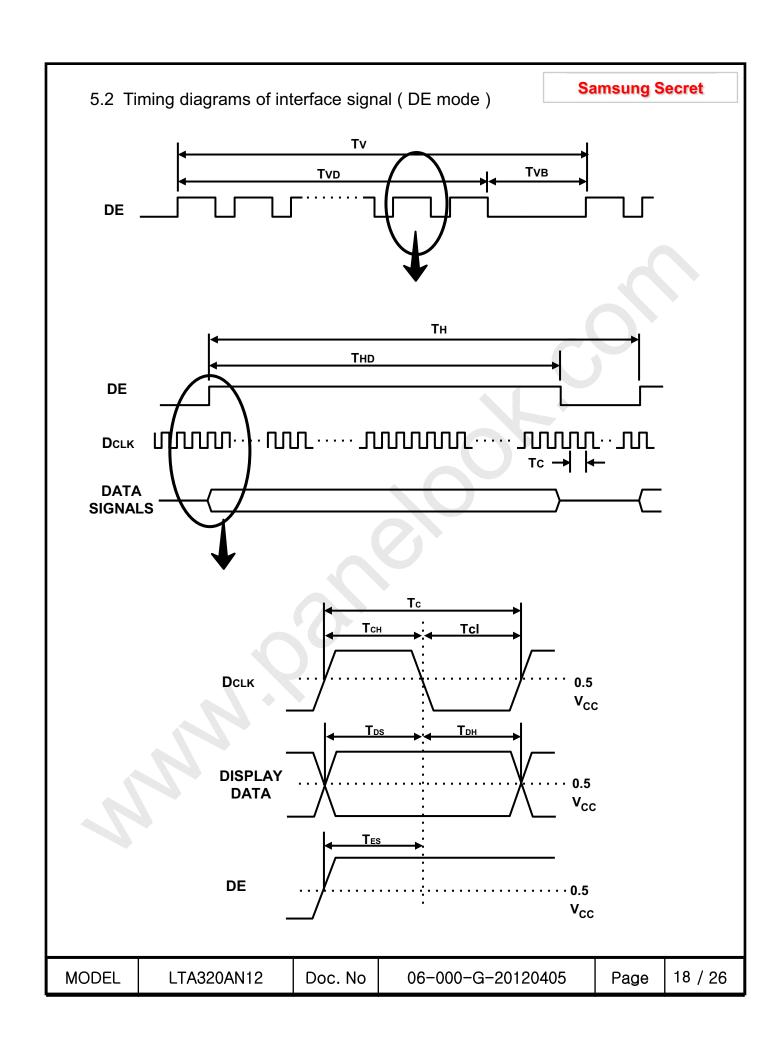
- (1) Test Point: TTL control signal and CLK at LVDS Tx input terminal in system
- (2) Internal VDD = 3.3V
- (3) Spread spectrum
  - Modulation rate (max) :  $\pm$  1.5 % , Modulation Frequency : under 100KHz

#### 5.2 LVDS Input Data Characteristics

ITE	ITEM		Min.	Тур.	Max.	UNIT	NOTE
Input Data Position	E -95MU-7	t <sub>RSRM</sub>	1	-	400	ps	
	F <sub>IN</sub> =85MHz	t <sub>RSLM</sub>	-400	1	1	ps	
Input common	Input common mode voltage		VssL+0.6	1.2	VDDL-0.6	V	-
Differential Input Voltage		V <sub>ID</sub>	100	-	-	mV	-

Note) When the skew is measured the Spread Spectrum should be 0%

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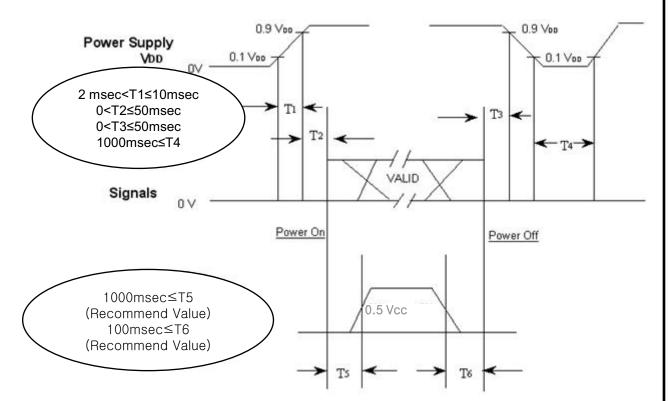




### 5.3 Power ON/OFF Sequence

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To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 :  $V_{DD}$  rising time from 10% to 90%

T2 : The time from  $V_{DD}$  to valid data at power ON.

T3 : The time from valid data off to  $V_{DD}$  off at power Off.

T4: V<sub>DD</sub> off time for Windows restart

T5: The time from valid data to B/L enable at power ON.

T6: The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V<sub>DD</sub>.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V<sub>DD</sub> = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.
- In Case T5 is less than 1000msec and T6 is less than 100msec,
   Garbage Display can be seen. (It is not related to electrical function issue, Just for recommendation to prevent Garbage Display )

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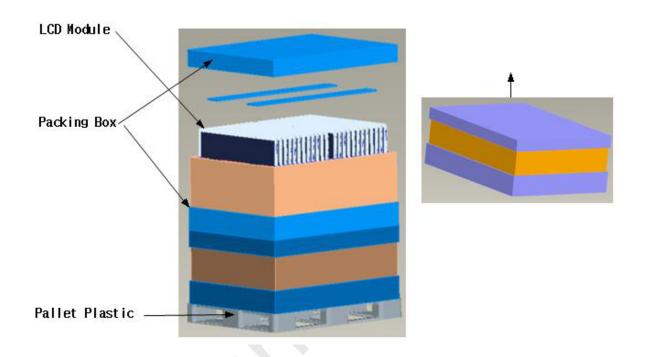
**Samsung Secret** 6. Outline Dimension- Front MODEL 20 / 26 LTA320AN12 Doc. No 06-000-G-20120405 Page

**Samsung Secret** 6. Outline Dimension-Rear MODEL 21 / 26 LTA320AN12 Doc. No 06-000-G-20120405 Page



### 7. PACKING

- 7.1 CARTON (Internal Package)
- (1) Packing Form
  Corrugated fiberboard box and corrugated cardboard as shock absorber
- (2) Packing Method



### 7.2 Packing Specification

	Item		Spe	cification	Remark			
	LCD Packing		24ea / Box 48ea / Pallet (Packing-Pallet Box)		- 5.5kg/LCD(48ea) : 264kg(Max.) - 15 kg / Packing Set : 30kg(Typ.) (1104X810X480) - Packing Material: Paper			
	Pallet		2 Box/Pallet		Pallet Weight: 5.3kg			
4	Packing Direction		Vertical		-			
	Total Pallet Size		1150 * 850 * 1083		Length x Width x Height			
	Total Pallet Weight 300.74		300.74	Module (5.5 x 48 ) + Pallet (5 SET (15) x2 + Desiccant (0.0	Ο,	~		
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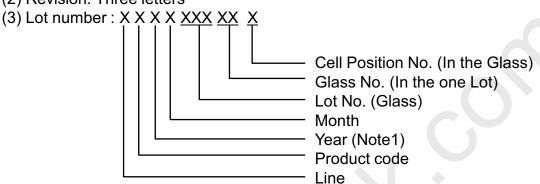
#### 8. MARKING & OTHERS

Global LCD Panel Exchange Center

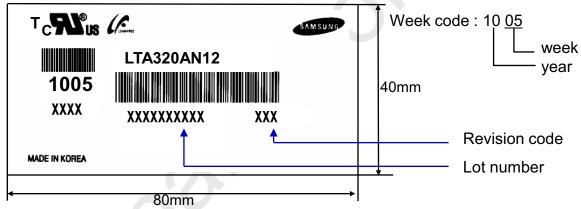
A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Part number: LTA320AN12

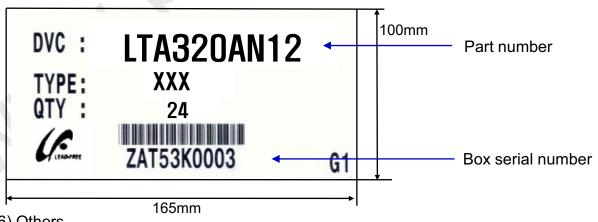
(2) Revision: Three letters



(4) Nameplate Indication



(5) Packing box attach



(6) Others

1. After service part Lamps cannot be replaced because of the narrow bezel structure.

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#### 9. General Precautions

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- 9.1 Handling
- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the converter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and LED back light.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not disassemble shield case of converter & C-PBA
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handling a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

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### 9.2 Storage

We highly recommend to comply with the criteria in the table below.

Item	Unit	Min.	Max.
Storage Temperature	(℃)	5	40
Storage Humidity	(%rH)	35	75
Storage Life	6 Months		
Storage Condition	control Products should not I from a wall Prevent products fror cautious of a build up of condens - Avoid other hazardou - If products delivered of 3 months, the recomme	ould provide good ventilate pe placed on the floor, but my direct sunlight, moistures ation. It is environment while store or kept in conditions of ownered temperature or huseave them at a temperat	re nor water; Be ring goods. ver the storage period midity range,

#### 9.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its converter power supply should be connected directly with a minimized length. A longer cable between the back light and the converter may cause lower luminance of LED and may require higher startup voltage(Vs).

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### 9.4 Operation Condition Guide

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(a) The LCD product should be operated under normal conditions. Normal condition is defined as below;

- Temperature : 20±15 °C- Humidity : 55±20%

- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

#### 9.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. ( supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
  Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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